## WHAT IS CLAIMED, IS

- 1. An electronic apparatus comprising:
- 5 a graphics memory storing a first and a second graphics object;
  - an OSD processor generating a first digital stream representing the first graphics object;
  - a pictures memory containing a picture and generating a second digital stream;
- a mixer able to mix the first digital stream and the second digital stream into a video signal;
  - means for converting the second graphics object into picture data;
  - means for writing the picture data to the picture memory.
- 15 2. An electronic apparatus according to Claim 1, comprising:
  - means for detecting overlaps between the first and the second graphics objects generating an overlap cue.
  - 3. An electronic apparatus according to Claim 2, comprising:
- means for controlling the mixer, means for conversion and means for writing as a function of the overlap cue.
  - 4. An electronic apparatus according to Claim 1, comprising:
  - a video memory supplied by a decoder and linked to the mixer.

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- 5. An electronic apparatus according to Claim 1, wherein the video signal is transmitted to an output connector.
- 6. An electronic apparatus according to Claim 1, wherein the means for30 converting the second graphics object into picture data are a piece of software executed by a main controller.

- 7. An electronic apparatus according to Claim 1, in which the picture memory is a stationary picture memory.
- 5 8. A process for generating a video signal, comprising the following steps:
  - generation by an OSD processor of a first digital stream representing a first graphics object;
  - conversion of a second graphics object into a picture;
- 10 writing of the picture to a memory;
  - generation of a second digital stream from the memory;
  - mixing of the first digital stream and of the second digital stream;
  - generation of a video signal from said mixture.
- 9. A process for generating a video signal according to Claim 8, wherein said mixture is produced with application of a transparency coefficient.
  - 10. A process for generating a video signal, comprising the following steps:
- reception of a command to display a first and a second graphics object;
  - detection of a possible overlap between the first and the second graphics object;
- if absence of overlap, generation by an OSD processor of a digital
  stream representing the first graphics object and the second graphics object, and generation of a video signal based on the digital stream;
  - if presence of an overlap:

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- generation by an OSD processor of a first digital stream representing a first graphics object;
  - conversion of the second graphics object into a picture;
  - writing of the picture to a memory;

- generation of a second digital stream from the memory;
- mixing of the first digital stream and of the second digital stream;
  - generation of a video signal from said mixture.

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- 11. Electronic apparatus comprising:
- an OSD processor managing an OSD plane;
- a mixer able to mix a picture plane and the OSD plane;
- means for converting a graphics object into picture data;
- 10 means for writing the picture data to the picture plane.